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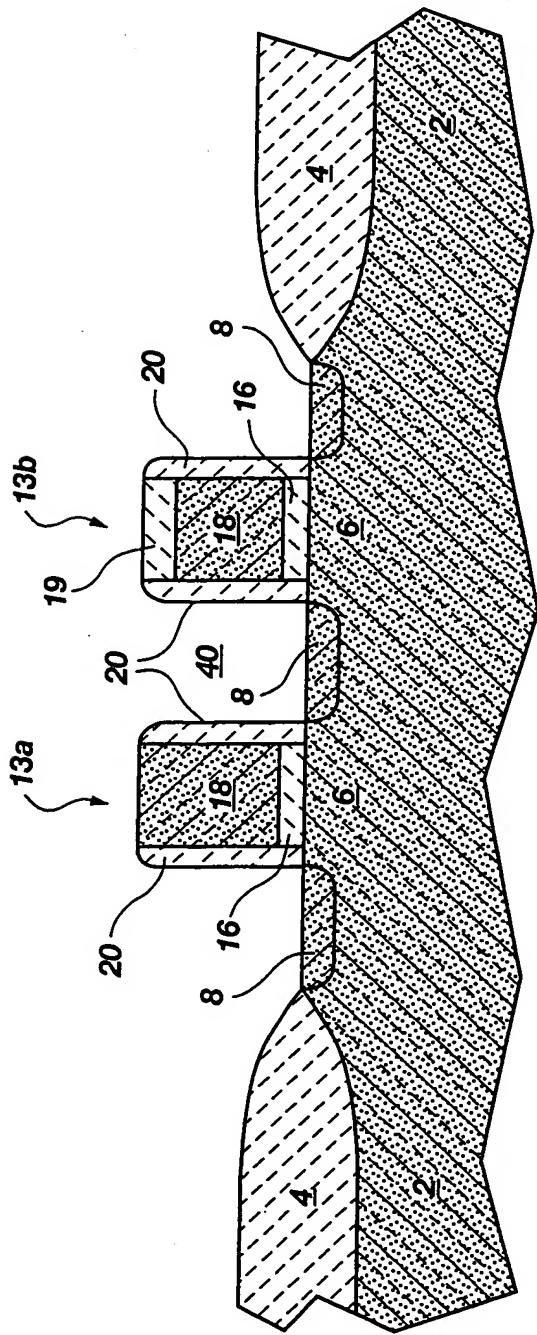


Fig. 1

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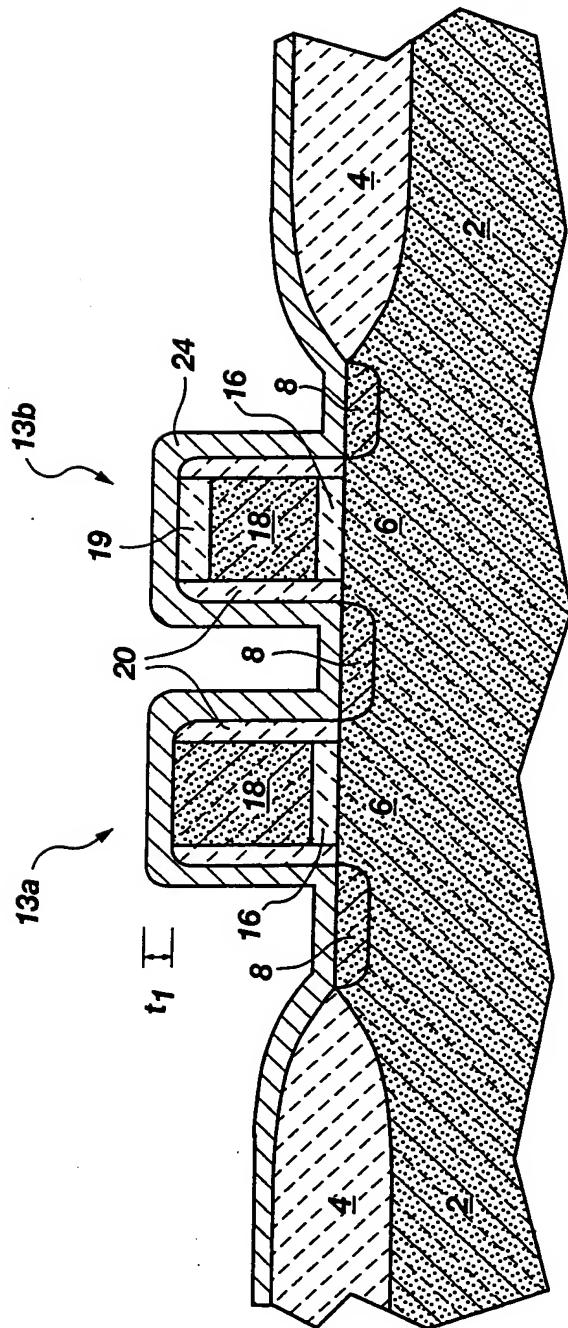


Fig. 2

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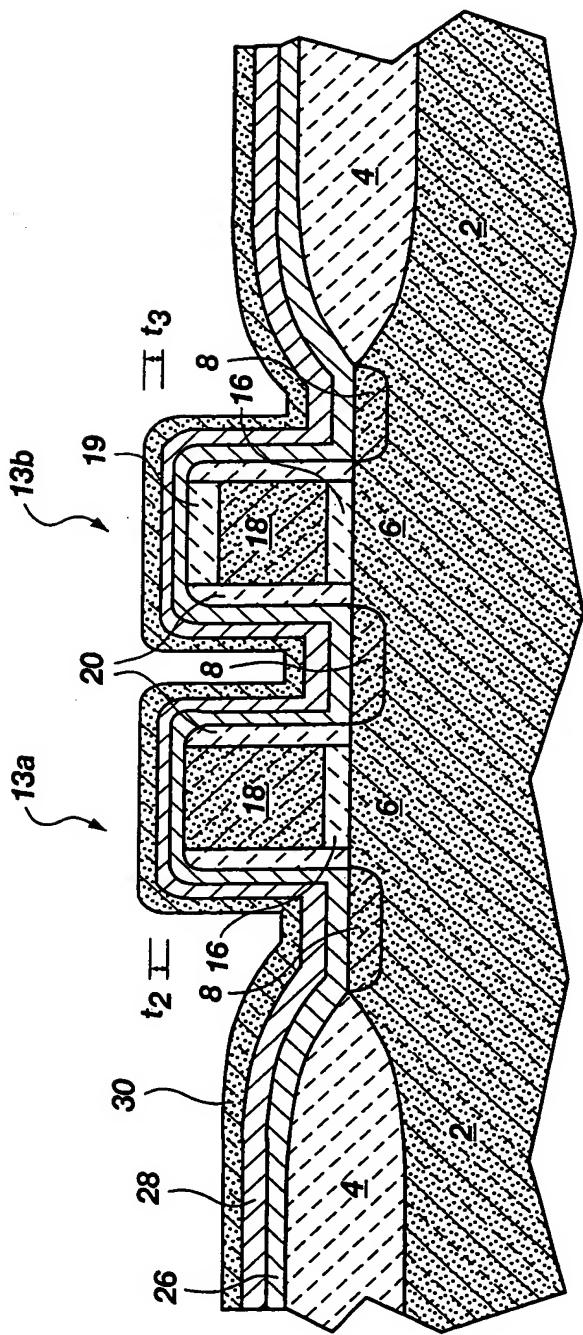
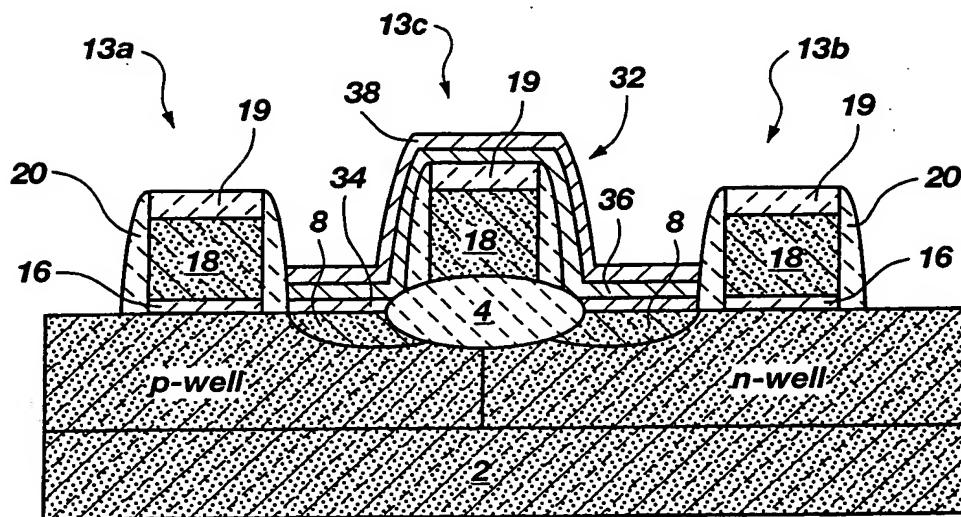


Fig. 3

TITLE: LOCAL INTERCONNECT STRUCTURE FOR  
INTEGRATED CIRCUIT DEVICES, SOURCE STRUCTURE FOR  
THE SAME, AND METHOD FOR FABRICATING THE SAME  
Inventor: Jigish D. Trivedi et al.  
Serial No.: Not Yet Assigned  
Docket No.: 3439.4US

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**Fig. 4**